102(a) as being clearly anticipated by the MultimediaCard specification Version 1.4. A Declaration submitted under 37 CFR 1.132 is included with this Amendment and states that the relevant portions of "The MultiMedia Card System Specification" of the MMCA Technical Committee, Version 1.4, are a publication of the applicants' own invention. As such, this material is not applicable as prior art under 35 U.S.C. 102(a) and the rejection of claims 1-27 is accordingly traversed as "the invention was not known or used by *others* ... before the invention thereof by the applicant for patent", in the language of 35 U.S.C. 102(a) (emphasis added).

Figures 1, 2, and 5-7 are prior art for the purposes of this application have been amended to add the legend Prior Art. If the proposed amendment is acceptable, it will be incorporated into formal drawings when these are submitted at the appropriate time. Figures 3, 4, and 8 are a portion of the applicants' invention and contribution to "The MultiMedia Card System Specification" of the MMCA Technical Committee, Version 1.4, and are, therefore not prior art.

Therefore, reconsideration of the Office Action's rejection of claims 1-27 is therefore respectfully requested and an early indication of their allowability is earnestly solicited.

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Respectfully submitted,

By: _____

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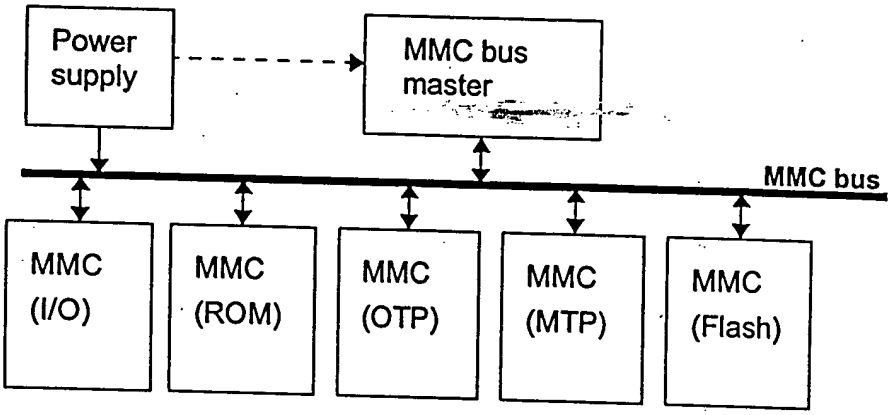

Fig 1

PRIOR ART

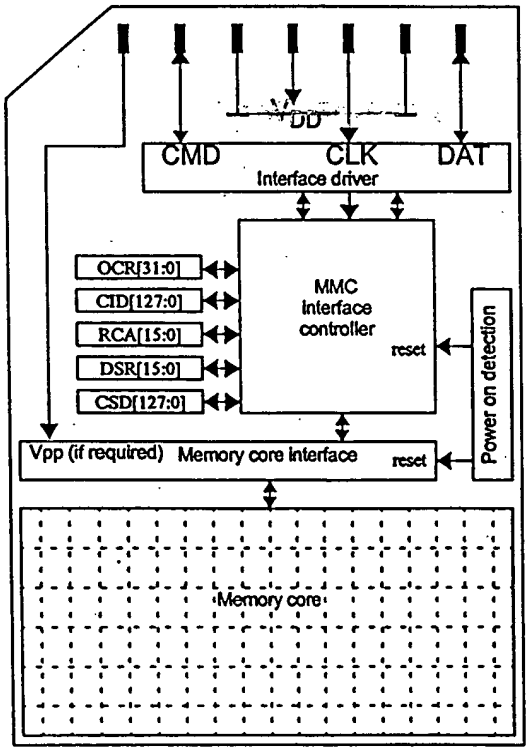

Fig 2

PRIOR ART

OCR bit position	VDD voltage window
0-7	reserved
8	2.0-2.1
9	2.1-2.2
10	2.2-2.3
11	2.3-2.4
12	2.4-2.5
13	2.5-2.6
14	2.6-2.7
15	2.7-2.8
16	2.8-2.9
17	2.9-3.0
18	3.0-3.1
19	3.1-3.2
20	3.2-3.3
21	3.3-3.4
22	3.4-3.5
23	3.5-3.6
24-30	reserved
31	card power up status bit (busy)[1]

Fig 5

PRIOR ART

Name	Field	Width	CID-slice
Manufacturer ID	MID	24	[127:104]
Card individual number	CIN	96	[103:8]
CRC7 checksum	CRC	7	[7:1]
not used, always '1'	-	1	[0:0]

Fig 6
PRIOR ART

Name	Field	Width	Cell Type	CSD-slice
CSD structure	CSD_STRUCTURE	2	R	[127:126]
MMC protocol version	MMC_PROT	4	R	[125:122]
reserved		2	R	[121:120]
data read access-time-1	TAAC	8	R	[119:112]
data read access-time-2 in CLK cycles (NSAC*100)	NSAC	8	R	[111:104]
max. data transfer rate	TRAN_SPEED	8	R	[103:96]
card command classes	CCC	12	R	[95:84]
max. read data block length	READ_BL_LEN	4	R	[83:80]
partial blocks for read allowed	READ_BL_PARTIAL	1	R	[79:79]
write block misalignment	WRITE_BLK_MISALIGN	1	R	[78:78]
read block misalignment	READ_BLK_MISALIGN	1	R	[77:77]
DSR implemented	DSR_IMP	1	R	[76:76]
external Vpp	VPROG	2	R	[75:74]
device size mantissa	C_SIZE_MANT	8	R	[73:66]
device size exponent	C_SIZE_EXP	4	R	[65:62]
max. read current @V_{DD} min	VDD_R_CURR_MIN	3	R	[61:59]
max. read current @V_{DD} max	VDD_R_CURR_MAX	3	R	[58:56]
max. write current @V_{DD} min	VDD_W_CURR_MIN	3	R	[55:53]
max. write current @V_{DD} max	VDD_W_CURR_MAX	3	R	[52:50]
max. V_{PP} current	VPP_CURR	3	R	[49:47]
erase sector size	SECTOR_SIZE	5	R	[46:42]
erase group size	ERASE_GRP_SIZE	5	R	[41:37]
write protect group size	WP_GRP_SIZE	5	R	[36:32]
write protect group enable	WP_GRP_ENABLE	1	R	[31:31]
manufacturer default ECC	DEFAULT_ECC	2	R	[30:29]
stream write speed factor	R2W_FACTOR	3	R	[28:26]
max. write data block length	WRITE_BL_LEN	4	R	[25:22]
partial blocks for write allowed	WRITE_BL_PARTIAL	1	R	[21:21]
reserved		5	R	[20:16]
reserved		3	R/W	[15:13]
copy flag (OTP)	COPY	1	R/W	[12:12]
permanent write protection	PERM_WRITE_PROTECT	1	R/W	[11:11]
temporary write protection	TMP_WRITE_PROTECT	1	R/W/E	[10:10]
ECC code	ECC	2	R/W/E	[9:8]
CRC	CRC	7	R/W/E	[7:1]
not used, always '1'	-	1	-	[0:0]

Fig 7
PRIOR ART